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PATENT APPLICATION

RESPONSE AFTER FINAL REJECTION  
EXPEDITED PROCEDURE  
TECHNOLOGY CENTER ART UNIT 2816

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Masahiko TSUCHIYA

Application No.: 09/817,193

Filed: March 27, 2001

For: DIFFERENTIAL AMPLIFIER, SEMICONDUCTOR DEVICE, POWER SUPPLY  
CIRCUIT AND ELECTRONIC EQUIPMENT USING THE SAME

Group Art Unit: 2816

Examiner: Q. Tra

Docket No.: 108097

AMENDMENT AFTER FINAL REJECTION  
UNDER 37 C.F.R. §1.116

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

In reply to the October 3, 2002 Office Action, please amend the above-identified  
application as follows:

IN THE CLAIMS:

Please replace claim 1 as follows:

1. (Twice Amended) A differential amplifier comprising:  
a first differential amplifier circuit having a first differential pair and operating based  
on a common input voltage;  
a second differential amplifier circuit having a second differential pair and operating  
based on the common input voltage;  
a third transistor of the primary conductive type which operates based on a first signal  
from the first differential amplifier; and

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Amended  
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